

Electronic Acknowledgement Receipt

EFS ID:	3840745
Application Number:	10525996
International Application Number:	
Confirmation Number:	6221
Title of Invention:	Semiconductor wafer protection structure and laminated protective sheet for use therein
First Named Inventor/Applicant Name:	Hideo Senoo
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Application Type:	U.S. National Stage under 35 USC 371

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Submitted with Payment	no
File Listing:	

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Request for Certificate of Correction	050618rcc.pdf	137560	no	2
			6f97e4438b5703f9d13a2e4d7cc1c133a8ec		
Warnings:					
Information:					
2	Miscellaneous Incoming Letter	050618coc.pdf	96038	no	1
			ee087e4ba2f1b4f93a73c4b48a7987ba5c41d44		
Warnings:					
Information:					
3	Miscellaneous Incoming Letter	050618efiledids.pdf	702714	no	8
			d4b49211b42272ef0e084300a50078830c7008d		
Warnings:					
Information:					
4	Miscellaneous Incoming Letter	050618amend.pdf	170427	no	2
			b4fed7686ec1338278e60853bb3aa65cc08749d2		
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Total Files Size (in bytes):			1106739		
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